

## Zeus, xGenius and Ether.Genius

|                                       | Zeus / xGenius  | Ether.Genius   |
|---------------------------------------|---|--|
| <b>Modularity</b>                     | Yes (through PHM-XX modules)  | Yes (Through AT-XX modules)  |
| <b>Maximum speed</b>                  | 1 Gb/s, upgradable to 10 Gb/s (xGenius only)  | 1 Gb/s   |
| <b>Traffic generation</b>             | Multiple traffic streams over two Ethernet ports  | Multiple traffic streams over a single Ethernet port   |
| <b>Ethernet automatic tests</b>       | RFC 2544, Y.1564, RFC 6349  | RFC 254  |
| <b>100 Mb/s optical Ethernet</b>      | Yes (dual port)   | Yes (dual port through special SGMII SFPs only)  |
| <b>GOOSE and SV analysis</b>          | Yes   | No   |
| <b>Traffic capture in PCAP format</b> | Yes, throughput is 10 Gb/s, hardware time-stamps synchronized with external clock reference)  | No   |
| <b>IEEE C37.94</b>                    | Yes (two ports, one-way and two-way delay, pass-through mode, jitter and wander, delay generation in loopback and pass-through modes)   | Yes (two ports, one-way and two-way delay, pass-through mode)  |
| <b>G.703 E0</b>                       | Yes (co-directional and contra-directional)   | Yes (co-directional)   |
| <b>E1 / T1</b>                        | Yes (dual port, BERT and performance, pulse mask, jitter and wander, add and drop, channel map, one-way and two-way tests, delay generation in loopback and pass-through modes) | Yes (dual port, BERT and performance, pulse mask, jitter and wander, add and drop, channel map, one-way and two-way tests) |
| <b>Voice Frequency</b>                | Yes (tone generation, noise and level measurements, frequency sweep, one-way and two-way delay)   | Yes (tone generation, noise and level measurements, frequency sweep, one-way and two-way delay)                            |
| <b>Datacom</b>                        | Yes (DTE / DCE emulation, BERT and performance, control circuit custom settings, one-way and two-way delay)   | Yes (DTE / DCE emulation, BERT and performance, control circuit custom settings, one-way and two-way delay)                |
| <b>Plots and traces</b>               | Yes (displays results in real time)   | Yes (displays only stored data)  |
| <b>GNSS clock reference input</b>     | Yes (GPS, GLONASS, Beidou, Galileo)   | Yes (GPS, GLONASS, Beidou, Galileo)  |
| <b>ToD clock references</b>           | Yes (built in the mainframe)  | Yes (available through the AT-96 module)   |
| <b>IRIG-B clock references</b>        | Yes (balanced / unbalanced reference input /output)   | No   |
| <b>Built in screenshot function</b>   | Yes   | No   |